

S E R V I C E N O T E

SUPERSEDES: None

**8922P Multiband Test Set**

**Serial Numbers:** 0000A00000 / 9999Z99999

**Service Information for verifying signals at System Bus connector**

**Duplicate Service Notes:**

8922P-04

**Situation:**

The 8922P/X #H09/K09 is a customer/Agilent installable retro-fit for testing of mobile phones capable of supporting Multi-Slot Data. Firmware should be C.01.06 or higher for this option. This option uses two 8922Ps operating as a system in a master/slave combination. The timing information required to synchronise the two instruments is sent between the system bus connectors of each instrument via a cable supplied with the upgrade kit. Should the need ever arise to check this port the following checks will verify instrument operation at the system bus connector.

*Continued*

DATE:March 1999

ADMINISTRATIVE INFORMATION

SERVICE NOTE CLASSIFICATION:		
<b>INFORMATION ONLY</b>		
AUTHOR:	ENTITY:	ADDITIONAL INFORMATION:
AS	E600	This will not be a high volume option. The user manual for #H09/K09 is 08922-90218

**Solution / Action:**

Service Information for verifying signals at System Bus connector. Output at pin 5 using DMM (Pin 5 normally TTL hi level).

## Check 1

Set service latch 'g\_run\_reset\_cntl = 0' on service screen.

Pin 5 should be at UL 0.8 VDC, LL 0 VDC.

## Check 2

Set service latch 'g\_run\_reset\_cntl = 1'.

Confirm Pin 5 high, ie voltage UL = 5 VDC, LL= 2.4 VDC.

Input level to pin 3 using DC level

## Check 3

Apply lo level (0.5VDC or ground) to Pin 3.

Confirm error 'Call disconnected:Ph error:0x009f' is seen on instrument screen

## Check 4

Apply high (5VDC) to Pin 3

Confirm no recurrence of error 'Call disconnected:Ph error:0x009f' on screen

Output at pin 24 using DMM (Pin 24 normally TTL hi level).

## Check 5

Set service latch 'g\_ext\_trig\_out = 0'.

Pin 24 should be at UL 0.8VDC, LL 0 VDC.

## Check 6

Set service latch 'g\_g\_ext\_trig\_out = 1'.

Confirm Pin 24 high, ie voltage UL = 5 VDC, LL= 2.4 VDC.

These checks confirm operation of the signals at the system bus connector.